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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned
Examiner: Unassigned

In Re PATENT APPLICATION Of:

Applicant(s): Nobuhisa KUMAMOTO et al.)
Serial No: 10/767,439)
Filed: January 30, 2004)
For: SEMICONDUCTOR CHIP AND)
PRODUCTION PROCESS)
THEREFOR)
Docket No.: AI 318D1)

**REQUEST TO CORRECT
SUBMITTED FORM PTO-1449**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is a request to correct an error in one of the publication numbers entered on a Form PTO-1449 filed with an Information Disclosure Statement in the U.S. Patent and Trademark Office on April 27, 2005.

The third publication number under "Foreign Patent Documents" on the Form PTO-1449 indicates an incorrect number. The number should be corrected to read:
Number: 0813 238. A new PTO-FORM-1449 is provided herewith correcting the error.
Request for notification of the correction is requested.

Respectfully submitted,

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SMR/pjl

May 26, 2005
Date

FEE ENCLOSED:\$
Please charge any further
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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT		Atty Docket		Application No.		
		AI 318D1		10/767,439		
		Applicant		Nobuhisa KUMAMOTO et al.		
		Filing Date January 30, 2004		Group Unit Unassigned		
U.S. PATENT DOCUMENTS						
Examiner Initial		Document Number	Date	Name	Class	
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
FOREIGN PATENT DOCUMENTS						
		Document Number	Date	Country	Class	
	AI	08-340029	12/24/96	JAPAN		
	AJ	9-330934	12/22/97	JAPAN		
	AK	0813 238	12/17/97	EUROPE		
	AL	4-99537	08/27/92	JAPAN		
	AM	7-42134	07/21/95	JAPAN		
	AN					
	AO					
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)						
	AP					
	AQ					
	AR					
Examiner				Date Considered		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.						